



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

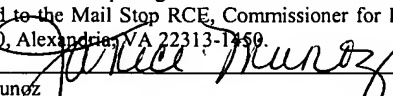
Applicant:	Jeffrey R. Wilcox, et al.	§	Group Art Unit:	2187
		§		
Serial No.:	10/038,960	§		
		§	Examiner:	Christian Chase
Filed:	January 2, 2002	§		
		§		
For:	Method and Apparatus For	§	Atty. Dkt. No.:	ITL.0668US (P12986)
	Reducing Power Consumption	§		
	In A Memory Bus Interface By	§		
	Selectively Disabling And	§		
	Enabling Sense Amplifiers	§		

Mail Stop RCE  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

PRELIMINARY AMENDMENT

Dear Sir:

Please amend the claims in accordance with the following CLAIM AMENDMENTS section; and consider the comments in the following REMARKS section.

Date of Deposit: December 22, 2004  
I hereby certify under 37 CFR 1.8(a) that this correspondence is being deposited with the United States Postal Service as **first class mail** with sufficient postage on the date indicated above and is addressed to the Mail Stop RCE, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.  
  
Janice Munoz